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Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

### **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



# DS9637A Dual Differential Line Receiver

## **General Description**

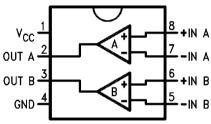
The DS9637A is a Schottky dual differential line receiver which has been specifically designed to satisfy the requirements of EIA Standards RS-422 and RS-423. In addition, the DS9637A satisfies the requirements of MIL-STD 188-114 and is compatible with the International Standard CCITT recommendations. The DS9637A is suitable for use as a line receiver in digital data systems, using either single ended or differential, unipolar or bipolar transmission. It requires a single 5V power supply and has Schottky TTL compatible outputs. The DS9637A has an operational input common mode range of ±7V either differentially or to ground.

#### **Features**

- Dual channel
- Single 5V supply
- Satisfies EIA standards RS-422 and RS423
- Built-in ±35 mV hysteresis
- High input common mode voltage range
- High input impedance
- TTL compatible outputs
- Schottky technology
- Extended temperature range

## **Connection Diagram**

#### 8-Lead DIP and SO-8 Package



962101

Top View
Order Number DS9637ACM or DS9637ACN
See NS Package Number M08A or N08E
For Complete Military Product Specifications,
refer to the appropriate SMD or MDS.
Order Number DS9637AMJ/883
See NS Package Number J08A

## **Absolute Maximum Ratings** (Note 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Range

Ceramic DIP -65°C to + 175°C Molded DIP -65°C to + 150°C

Lead Temperature

Ceramic DIP

300°C (Soldering, 30 seconds)

Molded DIP and SO Package

265°C (Soldering, 10 seconds)

Maximum Power Dissipation (Note 1) at 25°C

Cavity Package 1300 mW Molded Package 930 mW SO Package 810 mW

V<sub>CC</sub> Lead Potential to Ground -0.5V to 7.0V

Input Potential to Ground ±15V

Differential Input Voltage ±15V Output Potential to Ground -0.5V to +5.5V **Output Sink Current** 50 mA ESD Susceptibility, HBM ≥2 kV

## **Recommended Operating Conditions**

DS9637AM	Min	Max	Units
Supply Voltage (V <sub>CC</sub> )	4.5	5.5	V
Operating Temperature (T <sub>A</sub> )	-55	+125	°C
DS9637AC			
Supply Voltage (V <sub>CC</sub> )	4.75	5.25	V
Operating Temperature (T <sub>A</sub> )	0	+70	°C

Note 1: Derate cavity package 8.7 mW/°C above 25°C; derate molded DIP package 7.5 mW/°C above 25°C; derate SO package 6.5 mW/°C above 25°

#### Electrical Characteristics (Notes 3, 4)

Over recommended operating temperature and supply voltage ranges, unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V <sub>TH</sub>	Differential Input	$-7.0V \le V_{CM} \le +7.0V$	-0.2		+0.2	V
	Threshold Voltage (Note 6)					
V <sub>TH(R)</sub>	Differential Input	$-7.0V \le V_{CM} \le +7.0V$	-0.4		+0.4	V
	Threshold Voltage (Note 7)					
I <sub>1</sub>	Input Current	$V_{I} = 10V, 0V \le V_{CC} \le +5.5V$		1.1	3.25	mA
	(Note 8)	$V_{I} = -10V, 0V \le V_{CC} \le +5.5V$		-1.6	-3.25	
V <sub>OL</sub>	Output Voltage LOW	I <sub>OL</sub> = 20 mA, V <sub>CC</sub> = Min		0.35	0.5	V
V <sub>OH</sub>	Output Voltage HIGH	$I_{OH} = -1.0 \text{ mA}, V_{CC} = \text{Min}$	2.5	3.5		V
I <sub>os</sub>	Output Short Circuit	$V_O = 0V$ , $V_{CC} = Max$	-40	-75	-100	mA
	Current (Note 5)					
I <sub>cc</sub>	Supply Current	$V_{CC} = Max, V_I + = 0.5V,$		35	50	mA
		$V_{I}$ = GND				
V <sub>HYST</sub>	Input Hysteresis	V <sub>CM</sub> = ±7.0V (See Curves)		70		mV

Note 2: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 3: Unless otherwise specified Min/Max limits apply across the -55°C to +125°C temperature range for DS9637AM and across the 0°C to +70°C range for the DS9637ASC. All typicals are given for  $V_{CC} = 5V$  and  $T_A = 25$ °C.

Note 4: All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 5: Only one output at a time should be shorted.

Note 6:  $V_{DIFF}$  (Differential Input Voltage) =  $(V_1+) - (V_1-)$ .  $V_{CM}$  (Common Mode Input Voltage) =  $V_1+$  or  $V_1-$ .

Note 7:  $500\Omega \pm 1\%$  in series with inputs.

Note 8: The input not under test is tied to ground.

## **Switching Characteristics**

 $V_{CC} = 5.0V, T_A = 25^{\circ}C$ 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>PLH</sub>	Propagation Delay Time	See AC Test Circuit		15	25	ns
	Low to High					
t <sub>PHL</sub>	Propagation Delay Time	See AC Test Circuit		13	25	ns
	High to Low					

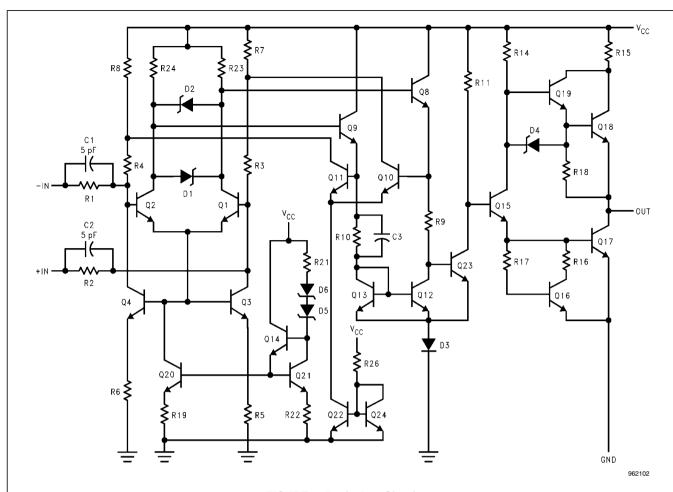
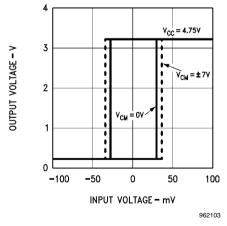
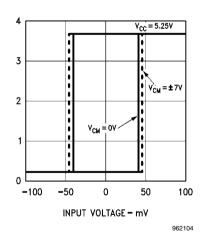


FIGURE 1. Equivalent Circuit

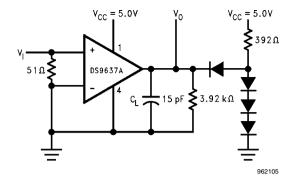
OUTPUT VOLTAGE - V

## **Typical Input/Output Transfer Characteristics**





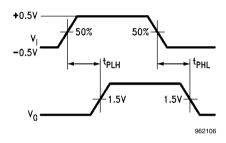
## **AC Test Circuit and Waveforms**



#### Notes:

 $\mathrm{C}_{\mathrm{L}}$  includes jig and probe capacitance. All diodes are FD700 or equivalent.

#### FIGURE 2.

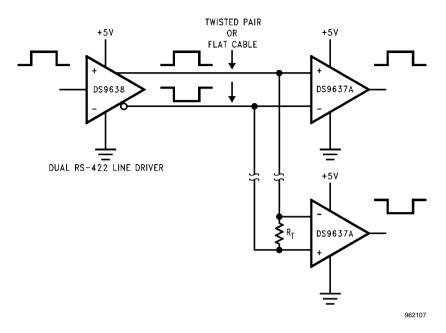


V,

Amplitude: 1.0V Offset: 0.5V Pulse Width: 100 ns PRR: 5.0 MHz  $t_r = t_i \le 5.0$  ns

FIGURE 3.

## **Typical Applications**



#### Notes:

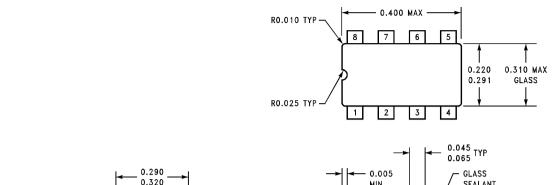
 $R_T \ge 50\Omega$  for RS-422 operation.

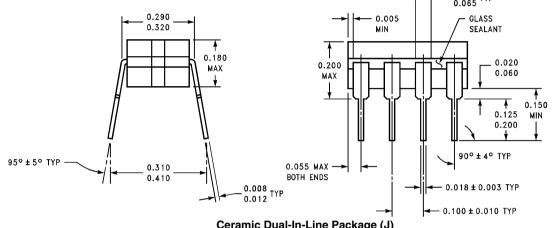
 $R_{T}^{\cdot}$  combined with input impedance of receivers must be greater than  $90\Omega.$ 

FIGURE 4. RS-422 System Application (FIPS 1020) Differential Simplex Bus Transmission

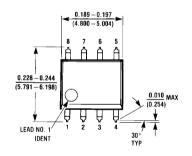
5

## Physical Dimensions inches (millimeters) unless otherwise noted

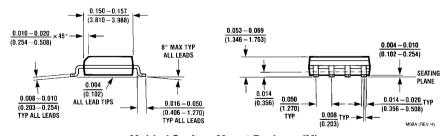




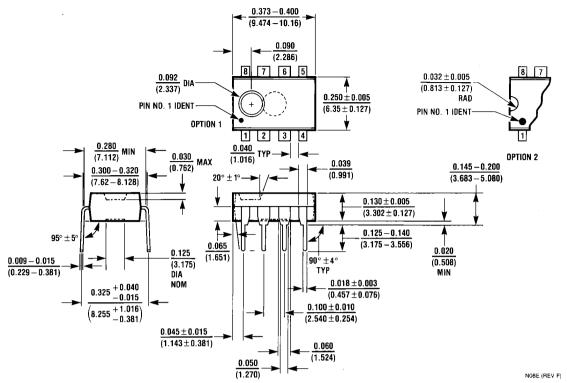
Ceramic Dual-In-Line Package (J) Order Number DS9637AMJ/883 NS Package Number J08A



JOSA (REV K)



Molded Surface Mount Package (M) Order Number DS9637ACM NS Package Number M08A



Molded Dual-In-Line Package (N) Order Number DS9637ACN NS Package Number N08E

## **Notes**

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