TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62304P,TD62304AP,TD62304F,TD62304AF TD62305P,TD62305AP,TD62305F,TD62305AF

7CH LOW ACTIVE DARLINGTON SINK DRIVER

The TD62304P/AP/F/AF and TD62305P/AP/F/AF are non-inverting transistor arrays, which are comprised of eight NPN darlington buffer-transistor output stages and PNP input stages.

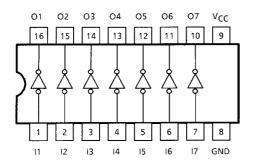
These devices can be operated by source input voltage and are suitable for operations with a 5-V general purposed logic IC such as 5-V TTL, 5-V CMOS and 5-V Microprocessor which have sink current output drivers.

Please observe the thermal condition for using.

FEATURES

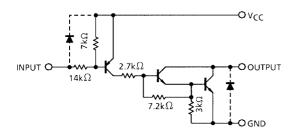
- Output current (single output) 500 mA (Max.)
- High sustaining voltage 35 V (TD62304P/F, 62305P/F) 50 V (TD62304AP/AF, 62305AP/AF) (Min.)
- Low level active input
- Input compatible with 5-V TTL and 5-V CMOS
- Package type-P, AP: DIP-16 pin
- Package type-F, AF: SOP-18 pin

PIN CONNECTION (TOP VIEW)

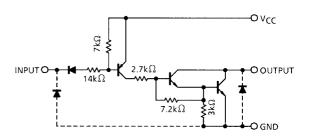


SCHEMATICS (EACH DRIVER)

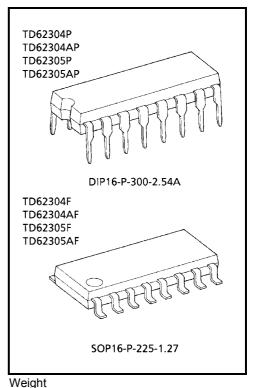
TD62034P/AP/F/AF



TD62305P/AP/F/AF



Note: The input and output parasitic diodes cannot be used as clamp diodes.



DIP16-P-300-2.54A : 1.11 g (Typ.) SOP16-P-225-1.27 : 0.16 g (Typ.)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	S	SYMBOL	RATING	UNIT	
Supply Voltage		V _{CC}	-0.5~7.0	V	
	P, F		-0.5~35		
Output Sustaining Voltage	AF	V _{CE (SUS)}	-0.5~50	V	
	AP		-0.5~50		
Output Current		I _{OUT}	500	mA / ch	
Input Voltage		V _{IN}	-22~V _{CC} +0.5	V	
		۷IN	-0.5~7 (Note 1)		
Input Current	out Current		-10	mA	
	Р		1.0	w	
Power Dissipation	AP	PD	1.47		
	F, AF		0.625 (Note 2)		
Operating Temperature	Р	т	-30~75	°C	
Operating Temperature		T _{opr}	-40~85	C	
Storage Temperature		T _{stg}	-55~150	°C	

Note 1: TD62305P / AP / F / AF

Note 2: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C and Ta = -30~75°C for only Type-P)

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT	
Supply Voltage	upply Voltage		V _{CC}	_		4.5	5.0	5.5	V
Output Sustaining Voltage AF		P, F			0	_	35	v	
		AF	V _{CE (SUS)}	_	0	_	50		
		AP				0	_		50
			lout	DC 1 Circuit		0	_	400	mA / ch
Output Current		Р		T _{pw} = 25 ms, duty = 10% 7 circuits		0	_	300	
		AP		T _{pw} = 25 ms, duty = 10% 7 circuits		0	_	350	
		AP		T _{pw} = 25 ms, duty = 20% 7 circuits		0	_	200	
		F/AF	/ AF	$\begin{array}{l} T_{pw} \leq 25 \text{ ms} \\ \text{7 circuits} \\ T_j = 120^\circ\text{C} \\ \text{Ta} = 85^\circ\text{C} (\text{Note 1}) \end{array}$	duty = 10%	0	_	240	-
					duty = 50%	0	—	60	
Input Voltage	TD62304P	/ AP / F / AF	V _{IN}	_		-20	_	V _{CC}	v
	TD62305P	/ AP / F / AF				0	-	5.5	
· · –		Р	PD	_		-	_	0.44	
		AP		_		_	—	0.52	W
		F, AF		(Note 1)			_	0.325	

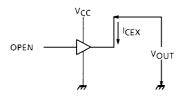
Note 1: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Output Leakage	P/F	ICEX	1	V _{CC} = 5.5 V V _{IN} = 0 V	V _{OUT} = 35 V Ta = 75°C	_	_	-100	μA
Current					V _{OUT} = 50 V Ta = 85°C				
Output Saturation Voltage		V _{CE (sat)}	2	V _{CC} = 4.5 V I _{OUT} = 350 mA	V _{IN} = V _{IN (ON)} MAX.	_	1.4	2.0	V
Input Current	(Output On)	I _{IN (ON)}	3	V _{CC} = 5.5 V, V _{IN} = 0.4 V		_	-0.32	-0.45	mA
	(Output Off)			V_{CC} = 5.5 V, V_{IN} = -20 V		_	-	-2.6	
	(Output Off)	I _{IN (OFF)}	4	_		—	_	-40	μA
Input Voltage (Output On)	TD62304	Variation	5	_		_	_	V _{CC} -2.8	v
	TD62305	Vin (ON)				_	_	V _{CC} -3.7	
	(Output On)	I _{CC (ON)}	6	V _{CC} = 5.5 V, V _{IN} = 0 V		—	17	22	mA
Supply Current	(Output Off)	I _{CC (OFF)}	6	V _{CC} = V _{IN} = 5.5 V		_	_	100	μA
Turn-On Delay	P, F	ton	- 7	V _{CC} = 5 V, C _L = 15 pF	V _{OUT} = 35 V R _L = 87.5 Ω		0.1	_	- µs
	AP, AF				V _{OUT} = 50 V R _L = 125 Ω				
Turn-Off Delay	P, F	toff			V _{OUT} = 35 V R _L = 87.5 Ω		3	_	
	AP, AF				V _{OUT} = 50 V R _L = 125 Ω		5		

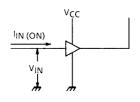
TEST CIRCUIT

1. ICEX

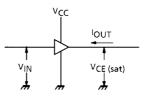




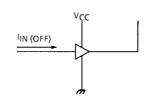
5. VIN (ON)



2. hFE, VCE (sat)







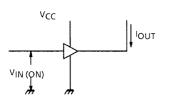
Vcc

l Icc

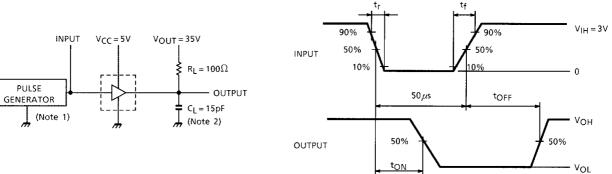
6. Icc

VIN (ON)

VIN (OFF)







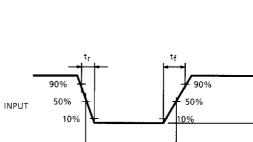
Note 1: Pulse Width 50 µs, duty cycle 10% Output impedance 50 Ω , t_r ≤ 10 ns, t_f ≤ 5 ns Note 2: CL includes probe and jig capacitance.

PRECAUTIONS for USING

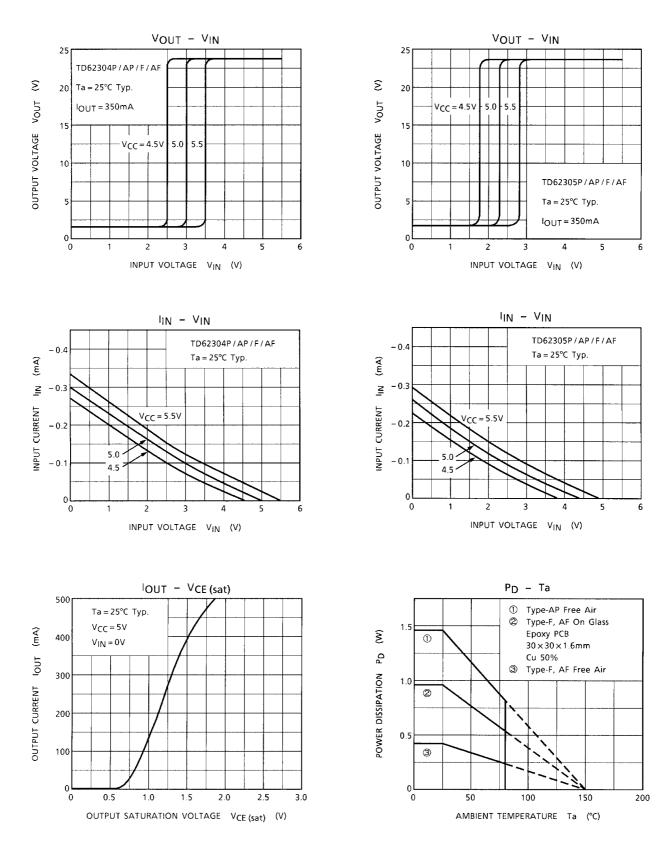
This IC does not include built-in protection circuits for excess current or overvoltage.

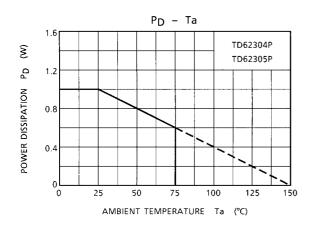
If this IC is subjected to excess current or overvoltage, it may be destroyed.

Hence, the utmost care must be taken when systems which incorporate this IC are designed.Utmost care is necessary in the design of the output line, VCC and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



OPEN

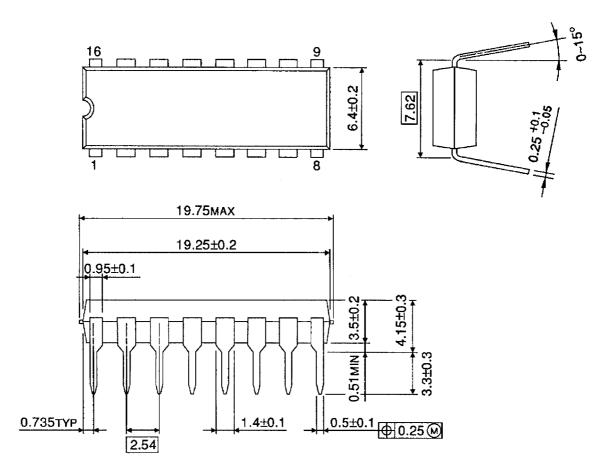




PACKAGE DIMENSIONS

DIP16-P-300-2.54A

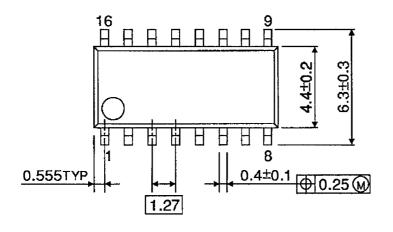
Unit : mm

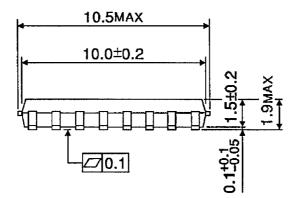


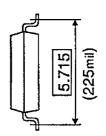
Weight: 1.11 g (Typ.)

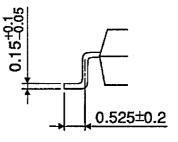
PACKAGE DIMENSIONS

SOP16-P-225-1.27









Weight: 0.16 g (Typ.)

Unit : mm

RESTRICTIONS ON PRODUCT USE

000707EBA

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